

AMENDMENTS IN THE SPECIFICATION

Please replace numbered paragraph 0019 on Page 6 with the following:

Figure 2B is a block diagram of a multiprocessor chip designed with dual IA-32 processors and a spare IA-32 processor to enable dynamic switching out of a failed processor during failure response in accordance with one embodiment of the present invention; and

Please replace numbered paragraph 0046 on Page 14 with the following:

The present invention provides several advantages over other available methods including: (1) no specialized software is needed because SMI is in BIOS, and the reaction to a failing processor is automatic. Also, (2) using an SMI, a service processor is able to identify a failing processor & halt it[[']]s execution from an outside perspective, unlike the limitation with a software limitation. Furthermore, (3) an alert about a hot-spare processor switching event can be generated.

Please replace numbered paragraph 0047 on Page 14 with the following:

The other software methods do not utilize SMIs or SMI functionality and encounter several drawbacks, including (1) customers must purchase the OS/software license that provides the hot-spare-processor feature, since not all OSes support this feature; and (2) the OS that has to make[[s]] the decision to switch to a hot spare processor may be running decision threads atop the faulting processor, leading to incorrect code execution.